

We claim:

1. A method of controlling a display, comprising:

(a) connecting a display controller to a CPU and to the display, the CPU having a progressively organized pixel memory, the controller capable of providing an interrupt; and

(b) scanning the pixel memory using the interrupt to control the scanning, thus providing scanned data to the display in a color field sequential mode.

2. The method of Claim 1, including providing that the interrupt is hardware-based.

3. The method of Claim 1, including providing that the interrupt is software-based.

4. The method of Claim 1, including providing that the CPU is a microprocessor.

5. The method of Claim 1, including providing a digital/analog converter between the controller and the display.

6. The method of Claim 5, including providing that the digital/analog converter comprises an R2R network.

7. A method of controlling a display, comprising:

(a) connecting a CPU having an integrated display controller to the display, the CPU having a progressively organized pixel memory, the controller capable of providing an interrupt; and

(b) scanning the pixel memory using the interrupt to control the scanning, thus providing scanned data to the display in a sequential mode.

8. A method of controlling a display, the method comprising:

(a) providing a central processing unit;

- (b) connecting a display controller to the central processing unit, the controller having an interrupt;
- (c) connecting the display to the display controller;
- (d) connecting a pixel memory to the display controller;
- (e) providing in the pixel memory a plurality of memory locations, each of which contains data corresponding to three primary colors;
- (f) sorting the data in the memory according to primary color;
- (g) scanning the data to provide an image of a first primary color on the display;
- (h) on completion of the first primary color image, using the interrupt to initiate in sequence formation of second and third primary color images, thus forming a multicolored image; and
- (i) after formation of the multicolored image, using the interrupt to initiate formation of further images.

9. A method of controlling a display, the method comprising:

- (a) providing a central processing unit;
- (b) connecting a display controller to the central processing unit, the controller having an interrupt;
- (c) connecting the display to the display controller;
- (d) connecting a pixel memory to the display controller;
- (e) providing in the pixel memory a plurality of memory locations, each of which contains data corresponding to three primary colors;

(f) selecting from the memory locations data corresponding to a first primary color and providing corresponding image signals to the display, thus forming a first primary color image;

(g) on completion of the first primary color image, using the interrupt to initiate in sequence formation of second and third primary color images, thus forming a multicolored image; and

(h) after formation of the multicolored image, using the interrupt to initiate formation of further images.

10. A method of controlling a display, the method comprising:

- (a) providing a central processing unit;
- (b) providing a display controller within the central processing unit, the display controller having an interrupt;
- (c) connecting the display to the display controller;
- (d) connecting a pixel memory to the display controller;
- (e) providing in the pixel memory a plurality of memory locations, each of which contains data on all three primary colors; and
- (f) scanning the memory locations to sequentially form image frames corresponding to each primary color, using the interrupt to initiate scanning for each image frame.

11. A system for controlling a display, comprising:

- (a) a display controller connected to the display;
- (b) a pixel memory connected to the display controller;
- (c) a plurality of memory locations in the pixel memory, each memory location containing data for three primary colors; and

(d) an interrupt in the controller, the interrupt configured to enable the pixel memory to be scanned such that the display sequentially forms images from data corresponding to each of the primary colors.

12. The system of Claim 11, including a digital/analog converter between the controller and the display.

13. The system of Claim 12, wherein the digital/analog converter comprises an R2R network.